Electronic Patent Application Fee Transmittal							
Application Number:	10709041						
Filing Date:	08-Apr-2004						
Title of Invention:	LOW TRIGGER VOLTAGE ESD NMOSFET TRIPLE-WELL CMOS DEVICES						
First Named Inventor/Applicant Name:	Kiran V. Chatty						
Filer:	Burton A. Amernick/Mary Ann Taylor						
Attorney Docket Number:	BUR920030188US1						
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Utility under 35 USC 111(a) Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:							
Pages:							
Claims:							
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Notice of appeal		1401	1	540	540		
Post-Allowance-and-Post-Issuance:							
Extension-of-Time:							

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
	Total in USD (\$)			540